

# ***Hierarchical Pointer Analysis for Distributed Programs***

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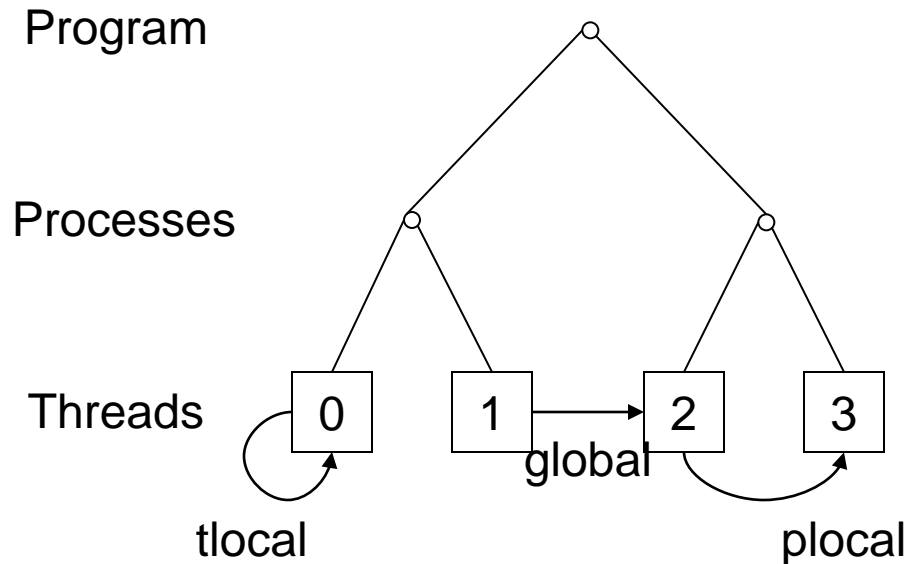
# *Background*

- **Titanium is a single program, multiple data (SPMD) dialect of Java**
  - All threads execute the same program text
- **Designed for distributed machines**
- **Global address space – all threads can access all memory**
- **At runtime, threads are grouped into processes**
  - A thread shares a physical address space with some other, but not all threads



# Memory Hierarchy

- **Global memory is composed of a hierarchy**



- **Locations can be thread-local (tlocal), process-local (plocal), or in another process (global)**



# Goal

- **Our goal is to produce a (flow-insensitive) pointer analysis that takes the memory hierarchy into account**
- **We define a small SPMD language based on Titanium**
- **We produce a type system that accounts for the memory hierarchy**
- **We give an overview of the abstract pointer analysis**



# Language Syntax

- **Types**

$\tau ::= \text{int} \mid \text{ref}_q \tau$

- **Qualifiers**

$q ::= \text{tlocal} \mid \text{plocal} \mid \text{global}$   
(tlocal @plocal @global)

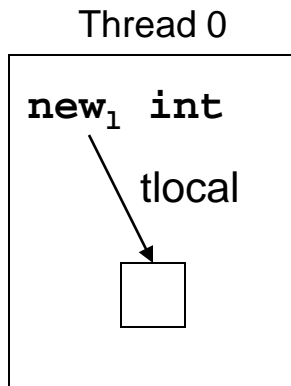
- **Expressions**

$e ::= \text{new}_1 \tau$  (allocation)  
| transmit  $e_1$  from  $e_2$  (communication)  
|  $e_1 \tilde{\wedge} e_2$  (dereferencing assignment)



# Type Rules – Allocation

- The expression  $\text{new}_l \tau$  allocates space of type  $\tau$  in local memory and returns a reference to the location
  - The label  $l$  is unique for each allocation site and will be used by the pointer analysis
  - The resulting reference is qualified with  $\text{tlocal}$ , since it references thread-local memory

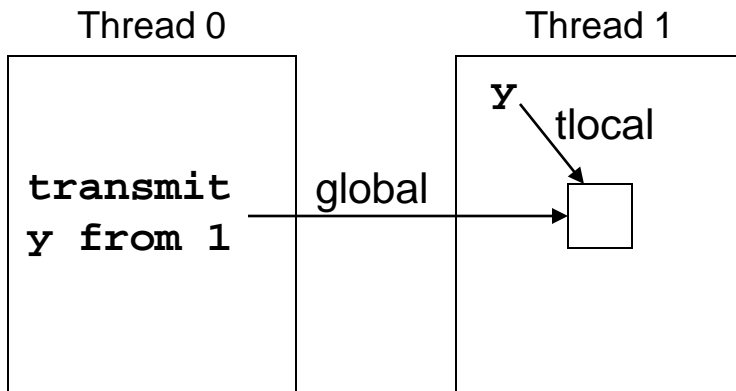


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$$\Gamma \vdash \text{new}_l \tau : \text{ref}_{\text{tlocal}} \tau$$


# Type Rules – Communication

- The expression `transmit e1 from e2` evaluates `e1` on the thread given by `e2` and retrieves the result
- If `e1` has reference type, the result type must be widened to global
  - Statically do not know source thread, so must assume it can be any thread



$$\Gamma \vdash e_1 : \tau \quad \Gamma \vdash e_2 : \text{int}$$


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$$\Gamma \vdash \text{transmit } e_1 \text{ from } e_2 : \text{expand}(\tau, \text{global})$$

$$\text{expand}(\text{ref}_q \tau, q') \quad \text{ref}_t(q, q') \tau$$

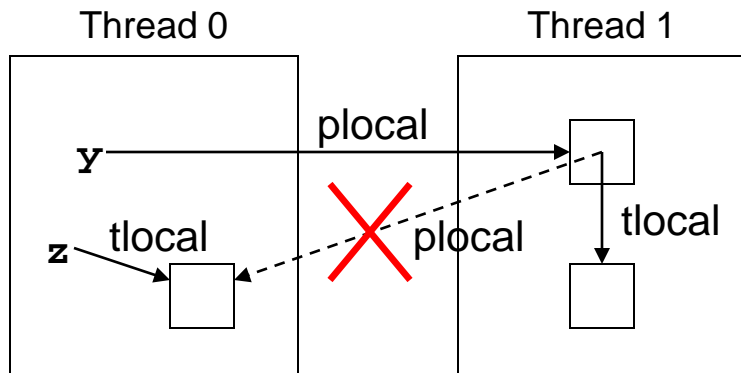
$$\text{expand}(\tau, q') \quad \tau \text{ otherwise}$$



## Type Rules – Dereferencing Assignment

- The expression  $e_1 \tilde{=} e_2$  puts the value of  $e_2$  into the location referenced by  $e_1$  (like  $*e_1 = e_2$  in C)
- If  $e_1$  has type  $\text{ref}_{\text{plocal}} \text{ref}_{\text{tlocal}} \tau$ , and  $e_2$  has type  $\text{ref}_{\text{tlocal}} \tau$ , the assignment could be unsound

$$\Gamma \vdash e_1 : \text{ref}_q \tau \quad \Gamma \vdash e_2 : \tau \quad \text{robust}(\tau, q)$$



$$\Gamma \vdash e_1 \tilde{=} e_2 : \text{ref}_q \tau$$

*robust*( $\text{ref}_q \tau, q'$ ) *false* if  $q @ q'$

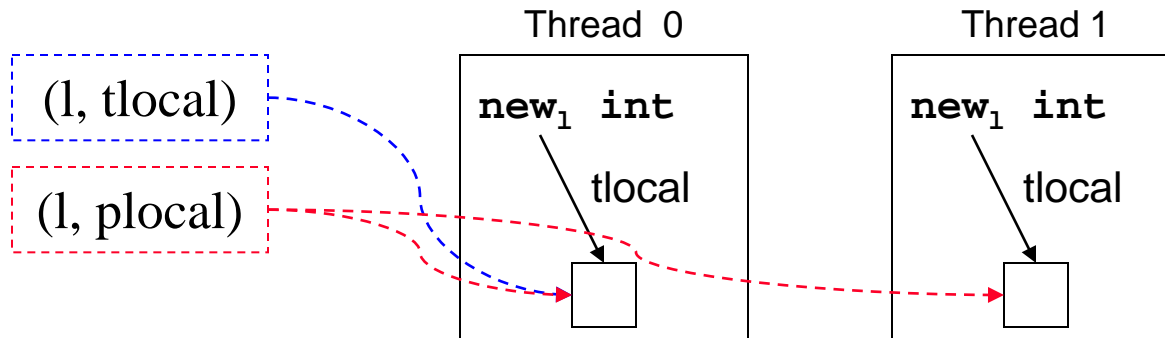
*robust*( $\tau, q'$ ) *true* otherwise





# Pointer Analysis

- Since language is SPMD, analysis is only done for a single thread (assume thread 0)
- Each expression has a points-to set of *abstract locations* that it can reference
- Abstract locations also have points-to sets
- Abstract locations consist of label and qualifier
  - A-loc  $(l, q)$  can refer to any concrete location allocated at label  $l$  and with type qualifier  $v \ q$  from thread 0



# *Pointer Analysis – Allocation and Communication*

- The abstract semantics for allocation and communication are similar to the type rules
- An allocation  $\text{new}_1 \tau$  produces a new abstract location  $(l, \text{tlocal})$
- The result of the expression  $\text{transmit } e_1 \text{ from } e_2$  is the global versions of the a-locs resulting from  $e_1$

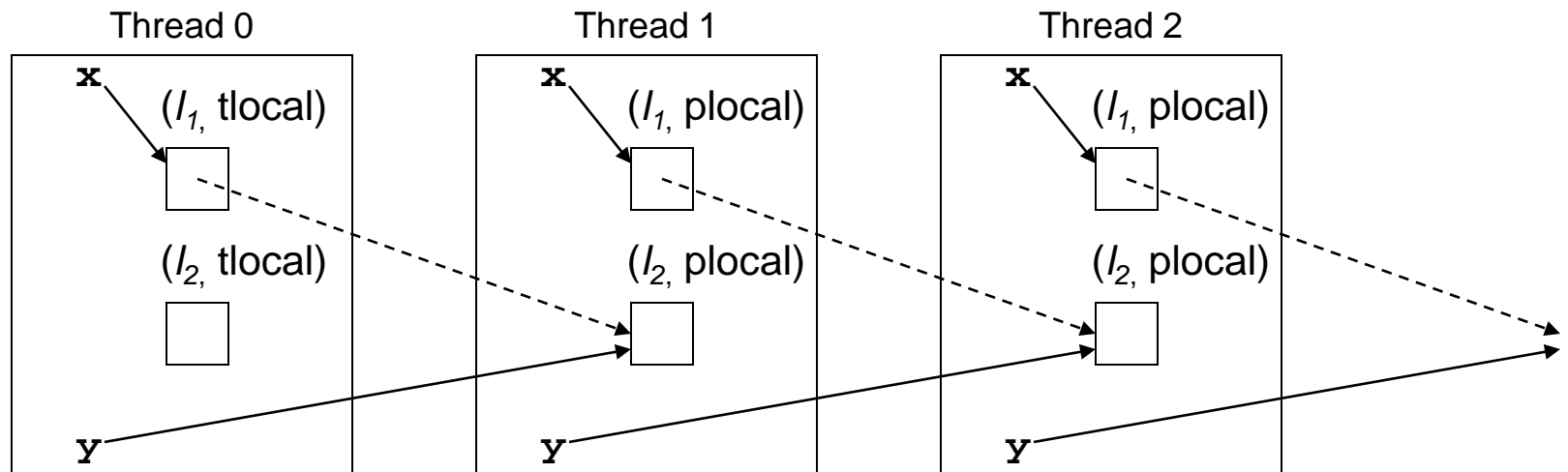
$e_1 ! \quad \{(l_1, \text{tlocal}), (l_2, \text{plocal}), (l_3, \text{global})\}$

$\text{transmit } e_1 \text{ from } e_2 ! \quad \{(l_1, \text{global}), (l_2, \text{global}), (l_3, \text{global})\}$



# Pointer Analysis – Dereferencing Assignment

- For assignment, must take into account actions of other threads



$x ! \{(l_1, \text{tlocal})\},$   
 $y ! \{(l_2, \text{plocal})\}$

$\rightarrow$

$x \tilde{A} y : (l_1, \text{tlocal}) ! (l_2, \text{plocal}),$   
 $(l_1, \text{plocal}) ! (l_2, \text{plocal}),$   
 $(l_1, \text{global}) ! (l_2, \text{global})$



## ***Race Detection Results***

- **Static race detection in Titanium using pointer analysis + concurrency analysis**
- **Most are false positives, so lower is better**

<b>Benchmark</b>	<b>No Pointer Analysis</b>	<b>One-Level Analysis</b>	<b>Two-Level Analysis</b>
<b>gas</b>	<b>2482</b>	<b>779</b>	<b>223</b>
<b>gsrb</b>	<b>512</b>	<b>187</b>	<b>18</b>
<b>lu-fact</b>	<b>490</b>	<b>177</b>	<b>1</b>
<b>pps</b>	<b>7026</b>	<b>1827</b>	<b>26</b>
<b>spm</b>	<b>443</b>	<b>177</b>	<b>0</b>

